## SSIRI 2011 Preliminary Program

Monday, June 27, 2011	
8:30 am - 9:00 am	Registration
9:00 am - 10:30 am	Opening Session
Seminar Room 1	SSIRI Steering Committee Chairs: W. Eric Wong and Sam Keene
	SSIRI 2011 General Chair: Yongrae Kwon
	SSIRI 2011 Program Chairs: Jongmoon Baik, Fabio Massacci and Mohammad Zulkernine
	Keynote I: Verifying Complex Software Systems: The Challenge
	Dr. Gerard J. Holzmann
	Jet Propulsion Laboratory
	NASA, USA
10:30 am – 11:00 am	Coffee Break
11:00 am – 12:30 pm	Session 1A: Secure Software Development
Seminar Room 1	
	Building a Secure System Model: Integrating Attack Trees with Statecharts
	Omar El Ariss, Jianfei Wu and Dianxiang Xu
	Probabilistic Risk Assessment for Security Requirements: A Preliminary Study
	Seok-Won Lee
	Component-Based Malicious Software Engineer Intrusion Detection
	Michael Shin, Snehadeep Sethia and Nipul Patel
11:00 am – 12:30 pm Seminar Room 2	Session 1B: Verification & Validation
Chair: Taisook Han	Execution Constraint Verification of Exception Handling on UML Sequence Diagrams
	Selim Ciraci, Hasan Sozer, Mehmet Aksit and Wilke Havinga
	Runtime Verification of Domain-Specific Models of Physical Characteristics in Control Software
	Arjan de Roo, Hasan Sozer and Mehmet Aksit
	Validation of SDL-based Architectural Design Models: New Coverage Criteria Andrew Restrepo and W. Eric Wong

11:00 am – 12:30 pm Seminar Room 3	Session 1C: Monitoring
	Using Partial Ordered Numbers to Control Information Flows Shih-Chien Chou
	Security Goals Assurance Based on Software Active Monitoring Changzhi Zhao and Wei Dong
	Model-Driven Monitoring of Time-Critical Systems based on Aspect-Oriented Programming Ki-seong Lee and Chan-Gun Lee
12:30 pm – 1:30 pm	Lunch Break
1:30 pm – 3:00 pm Seminar Room 1	Session 2A: Security Modeling
Chair: Masimo Flici	A Feature-Based Modeling Approach for Building Hybrid Access Control Systems
	Sangsig Kim, Dae-Kyoo Kim, Lunjin Lu, Sooyong park and Suntae Kim
	A Practical Covert Channel Identification Approach in Source Code based on Directed Information Flow Graph
	Jingzheng Wu, Yongji Wang, Liping Ding and Wei Han
	Comprehensive Two-level Analysis of Static and Dynamic RBAC Constraints with UML and OCL
	Mirco Kuhlmann, Karsten Sohr and Martin Gogolla
1:30 pm – 3:00 pm Seminar Room 2	Session 2B: Software Quality Analysis
Seminar Room 2	Deriving Data Dependence from/for UML State Machine Diagrams
	Hyeon-Jeong Kim, Doo-Hwan Bae, Vidroha Debroy and W. Eric Wong
	Automatic Synthesis of Static Fault Trees from System Models
	Jianwen Xiang and Kazuo Yanoo
	Evaluation of Experiences from Applying the PREDIQT Method in an Industrial Case Study Aida Omerovic, Bjørnar Solhaug and Ketil Stølen

1:30 pm – 3:00 pm Seminar Room 3	Session 2C: Software Testing
Seminar Room 3	On Testing Effectiveness of Metamorphic Relations: A Case Study
	Mahmuda Asrafi, Huai Liu and Fei-Ching Kuo
	Mutation-Based Evaluation of Weighted Test Case Selection for Firewall Testing
	Tugkan Tuglular and Gurcan Gercek
	RELEASE: Generating Exploits Using Loop-Aware Concolic Execution
	Bing-Han Li and Shiuhpyng Shieh
3:00 pm – 3:30 pm	Coffee Break
3:30 pm – 5:30 pm Seminar Room 1	Session 3A: Reliability Improvement
Chair: Dianxiang Xu	Towards a Reliable Spam-Proof Tagging System
Chair. Dianxiang Xu	Ennan Zhai, Liping Ding and Sihan Qing
	Dynamic Service Replacement to Improve Composite Service Reliability
	Jong-Phil Kim and Jang-Eui Hong
	ReLACK: A Reliable VoIP Steganography Approach
	Mohammad Hamdaqa and Ladan Tahvildari
3:30 pm – 5:30 pm	Session 3B: Safety & Validation
Seminar Room 2	
	Safe Software: Does it Cost More to Develop?
	W. Eric Wong, Andrea Demel, Vidroha Debroy and Michael Siok
	An Organization-Driven Approach for Enterprise Security Development and Management
	Lirong Dai and Yan Bai
	Trust Observations in Weli Lation Engenies
	Trust Observations in Validation Exercises Federica Amato, Massimo Felici, Paola Lanzi, Giulia Lotti and Luca Save
3:30 pm – 5:30 pm	Session 3C: Fast Abstract
Seminar Room 3	
	Towards Denotational Semantics for Verilog in PVS
	Han Zhu, Huibiao Zhu, Si Liu and Jian Guo

6:00 pm – 8:00 pm	Conference Reception
	Xin Chen, Nan Ye, Peng Jiang, Lei Bu and Xuandong Li
	Feedback-directed test case generation based on UML activity diagrams
	Atsuo Hazeyama
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	A Learning Environment for Software Security Education
	Kumiko Tadano, Jianwen Xiang, Masahiro Kawato and Yoshiharu Maeno
	Synthesizing SRN Models from System Operations with SysML Diagrams for Availability Analysis
	Dan Yu and Shilong Ma
	Analysis and Verification of Safety-Critical Software

	Tuesday, June 28, 2011
9:00 am - 10:00 am	Keynote II: Software Engineering Approaches to the Challenges in Technology Education and System Development in the
Seminar Room 1	Software Ecosystem Environment
Chair: W. Eric Wong	
	Professor C. V. Ramamoorthy
	Department of Electrical Engineering and Computer Sciences
	University of California at Berkeley, USA
10:00 am – 10:30 am	Coffee Break
10:30 am – 12:00 pm	Session 4A: DSDSD Workshop - I
Seminar Room 1	
	Formal Approaches to Wireless Sensor Networks
	Si Liu, Xiaofeng Wu, Qin Li, Huibiao Zhu and Qian Wang
	Case Study on Installing a Porting Process for Embedded Operating System in a Small Team
	DongSeok Cho and Doo-Hwan Bae
10:30 am – 12:00 pm	Session 4B: Student Doctoral Program
Seminar Room 2	
	Secure machine Learning, A Brief Overview
	Xiaofeng Liao, Youngji Wang and Liping Ding
	Verifying the Safety of Xen Security Modules
	Wei Han, Yeping He and Liping Ding
	Wei Hun, Teping He and Elping Ding
	Study on Formal Specification of Automatic Train Protection and Block System for Local Line
	Xie guo, Asano Akira, Takahashi Sei and Nakamura Hideo
	Specification and Verification of UML2.0 Sequence Diagrams Based on Event Deterministic Finite Automata
	Zhang Chen and Duan Zhenhua
10:30 am – 12:00 pm	Session 4C: SSCPS Workshop - I
Seminar Room 3	
	Static Data Race Detection for Interrupt-driven Embedded Software
	Rui Chen, Xiangying Guo, Yonghao Duan, Bin Gu and Mengfei Yang
	Approximate Simulation for Metric Hybrid Input/Output Automata
	Pan Haiyu and Zhang Min

	An Instrumentation Tool for Program Dynamic Analysis
	Chen Huajie, Zhang Tian and Li Xuandong
12:00 pm – 1:30 pm	Lunch Break
1:30 pm – 3:00 pm Seminar Room 1	Session 5A: DSDSD Workshop - II
	A Domain Specific Safety Analysis Technique for Digital Nuclear Plant Protection Systems Sanghyun Yoon, Jaeyeon Jo and Junbeom Yoo
	Multi-Layered Adaptive Monitoring in Service Robots
	Hwangwook Kim, Hyunji Yoon, Youngdo Cho, Sooyong Park and Vijayan Sugumaran
1:30 pm – 3:00 pm Seminar Room 2	Session 5B: Invited Session
	Challenges in Automatic Testing Sung Kim
	A Reliability Model for Complex Systems Norm Schneidewind and Mike Hinchey
	A Scalability Framework for Reliable Services Jae Youu Lee and Soo Dong Kim
1:30 pm – 3:00 pm Seminar Room 3	Session 5C SSCPS Workshop - II
Seminar Koom 5	A Methodology of Model-Based Testing for AADL Flow Latency in CPS Yufeng Zhu, Yunwei Dong, Chunyan Ma and Fan Zhang
	Security Requirements Analysis, Specification, Prioritization and Policy Development in Cyber-Physical Systems Kenneth Fletcher and Xiaoqing Frank Liu
	Approximating quantified SMT-Solving with SAT Xianjin Fu, Ji Wang, Wanwei Liu and Jing Li
3:00 pm – 3:30 pm	Coffee Break

3:30 pm – 5:30 pm Seminar Room 1	Session 6A: MVV Workshop - I
	Using a DSL and Fine-grained Model Transformations to Explore the Boundaries of Model Verification Marcel van Amstel, Mark van den Brand and Luc Engelen
	Coping with Complexity of Testing Models for Real-Time Embedded Systems Ralf Mitsching, Carsten Weise, Dominik Franke and Thomas Gerlitz
	Integrating DSL-CBI and NuSMV for Modeling and Verifying Interlocking Systems Yan Cao, Qiuzi Lu, Tianhua Xu, Tao Tang, Haifeng Wang and Yongcheng Xu
3:30 pm – 5:30 pm	Session 6B: MBDA Workshop - I
Seminar Room 2	Enhancing Use Cases with Risk by Subjective Risk Assessment Method Oluwasefunmi Arogundade, Zhi Jin and Xiaoguang Yang
	A UML Model based White Box Reliability Prediction to Identify Unreliable Components Daeeui Hong, Taewan Gu and Jongmoon Baik
	A SysML-based Requirement Supporting Tool for Embedded Software Chih-Hung Chang, Chih Wei Lu, William C. Chu, Chao-Tung Yang, Nien-Lin Hsueh, Pao-Ann Hsiung, Chorng-Shiuh Koong
3:30 pm – 5:30 pm Seminar Room 3	Session 6C: SSCPS Workshop - III
Seminar Room 3	An Embedded Operating System Design for the Lunar Exploration Rover CPS Lei Qiao, Mengfei Yang, Bin Gu, Hua Yang and Bo Liu
	MDRTGen: Automatic Regression Test Selection based on Activity Diagrams Nan Ye, Xin Chen, Peng Jiang, Wenxu Ding and Xuandong Li
6:00 pm – 10:00 pm	Conference Banquet

Wednesday, June 29, 2011	
9:00 am - 10:00 am	Keynote III: Evolving Critical Systems
Seminar Room 1	
	Professor Mike Hinchey
	University of Limerick, Ireland
10:00 am – 10:30 am	Coffee Break
10:30 am – 12:00 pm	Session 7A: MVV Workshop - II
Seminar Room 1	A Model for Usage-based Testing of Event-driven Software
	Steffen Herbold, Jens Grabowski and Stephan Waack
	Sterren Herbond, sens Grubowski und Stephan Wadek
	Handling periodic properties in software integration: deductive verification approach
	Alexander Bolotov
10:30 am – 12:00 pm	Session 7B: MBDA Workshop -II
Seminar Room 2	A Modeling Framework to Support Internal Control
	Takafumi Komoto, Kenji Taguchi, Haralambos Mouratidis, Nobukazu Yoshioka and Kokichi Futatsugi
	DRiVeR: Diagnosing Runtime Property Violations based on Dependency Rules
	Yanbin Liu, Ye Yang and Qiusong Yang
10:30 am – 12:00 pm	Session 7C: Software Safety in Practice
Seminar Room 3	Kounoto: Engineering Solf healing and Solf improving Systems
	Keynote: Engineering Self-healing and Self-improving Systems
	David Garlan
	School of Computer Science
	Carnegie Mellon University, USA
12:00 pm – 12:30 pm	Closing Session & End of SSIRI 2011